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	Filing Date	2003-06-24
	First Named Inventor	Michael B. Doerr
	Art Unit	2181
	Examiner Name	Meonske, Tonia L.
	Attorney Docket Number	5860-00101

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TLMD	5	Barua et al., "Compiler Support for Scalable and Efficient Memory Systems," IEEE Transactions on Computers, November 2001, 32 pages.	☐
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TLMD	16	Taylor et al., "How to build scalable on-chip ILP networks for a decentralized architecture," MIT/LCS Technical Memo MIT-LCS-TM-628, April 2000, 15 pages.	☐
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